

MEMORIU ȘTIINȚIFIC

Drd. Cristian STANCU

Studii de doctorat

Perioada: 2021-2026

Conducător științific: Prof.dr.ing. Lidia DOBRESCU

Domeniu: Inginerie Electronică Telecomunicații și Tehnologii Informaționale

Titlul tezei de doctorat: Amplificatoare Operaționale cu performanțe îmbunătățite

Activitate științifică

A. Lista de lucrări științifice (articole în reviste sau volume)

1. Stancu, C.; Dobrescu, D.; Dobrescu, L., Offset Voltage Reduction Methods for a Two-Stage Folded Cascode Operational Amplifier, 2022 14th International Conference on Electronics, Computers and Artificial Intelligence (ECAI), Ploiesti, Romania, 2022, pp. 1-4.
2. Stancu, C.; Neacsu, A.; Profirescu, O.; Dobrescu, D.; Dobrescu, L., Temperature and Power Supply Compensated CMOS Clock Circuit Based on Ring Oscillator, Electronics 2023, 12, (3), 507.
3. Stancu, C.; Neacsu, A.; Ionescu, T.; Stanescu, C.; Profirescu, O.; Dobrescu, D.; Dobrescu, L., Offset Voltage Reduction in Two-Stage Folded-Cascode Operational Amplifier Using High-Precision Source Degeneration, Electronics 2023, 12, (21), 4534.
4. Stancu, C.; Mitu, A.A.; Ionescu, T.; Neacsu, A.; Dobrescu, L.; Dobrescu, D., Enhanced Charge Pump Architecture with Feedback Supply Selector for Optimized Switching Performance, Electronics 2025, 14, (7), 1484.
5. Stancu, C.; Mitu, A.A.; Neacsu, A.; Dobrescu, L.; Dobrescu, D., Optimized Current-Source based on Brokaw Architecture for Constant Input Transistors Transconductance, 2025 17th International Conference on Electronics, Computers and Artificial Intelligence (ECAI), Targoviste, Romania, 2025, pp. 1-4.
6. Stancu, C.; Voicu, A.; Profirescu, O.; Dobrescu, L.; Dobrescu, D., Slew Rate Enhancement Circuit for Improved Transient Response in Folded Cascode Operational Amplifiers, 2025 International Semiconductor Conference (CAS), Sinaia, Romania, 2025, pp. 385-388.
7. Voicu, A.; Stancu, C.; Dobrescu, L.; Dobrescu, D., Current Mirror Precision in CMOS: Beyond Threshold Voltage Mismatch, 2025 14th International Symposium on Advanced Topics in Electrical Engineering (ATEE), Bucharest, Romania, 2025, pp. 1-4.
8. Voicu, A.; Stancu, C.; Profirescu, O.-G.; Dobrescu, L.; Dobrescu, D.; Dima, G., Replica-Based Bidirectional Output Current Limiting for High-Reliability CMOS Class AB Stages, Electronics 2026, 15, (8), 1595.

B. Participări la conferințe/workshop-uri

1. 14th International Conference on Electronics, Computers and Artificial Intelligence (ECAI), Ploiesti, Romania, 30 June – 1 July 2022.
2. 17th International Conference on Electronics, Computers and Artificial Intelligence (ECAI), Targoviste, Romania, 26 – 27 June 2025.
3. International Semiconductor Conference (CAS), Sinaia, Romania, 7 – 11 October 2025
4. 14th International Symposium on Advanced Topics in Electrical Engineering (ATEE), Bucharest, Romania, 9 – 11 October 2025.

Student-doctorand

Ing. Cristian STANCU

